



Docket No.: 069804-0015

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	<b>Response Under 37 C.F.R. § 1.116</b>
	:	<b><u>Expedited Procedure</u></b>
Mitsutoshi FUJITA, et al.	:	
	:	Customer Number: 2077
Application No.: 10/815,690	:	Confirmation Number: 9744
	:	
Filed: April 02, 2004	:	Group Art Unit: 2891
	:	Examiner: Caridad EVERHART

For: METHOD OF DESIGNING LOW-POWER SEMICONDUCTOR INTEGRATED CIRCUIT

**AMENDMENT UNDER 37 C.F.R. § 1.116**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the final Office Action dated November 29, 2005, having a three-month shortened statutory period for reply set to expire on February 28, 2006, please amend the above identified application as follows: